Capacitors | Miniature Micro-Layer Caps | Film vs Ceramic

* CL2 Dielectric: BX, 2C1, X7R

Criteria	Ceramic	MML™
Energy density	Limited	Very Good
Operating temp	Up to 125°C	Up to 140°C
Capacitance Drift (T) (-55°C to 125°C)	High especially on CL2* (+15% to -15%)	Low (-4% to +2%)
Capacitance Drift (V) (@ rated voltage)	High especially on CL2* (-25% to -30%)	No drift
Dielectric Losses (δ)	High especially on CL2* (X7R typical $150x10^{-4}$)	Low $60 X 10^{-4}$
Weight	High	Low
Failure Mode	Short Circuit	Open Circuit
Self Healing	None	Excellent

Similar footprint could be study

Outstanding

Weakness

Favorable performance characteristics compared to ceramic technologies